

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Yasuji Norito

Serial No.: 10/668,254

Group Art Unit: 2816

Filed: September 24, 2003

Examiner: Unknown

For: HYSTERESIS CHARACTERISTIC SETTING DEVICE AND
HYSTERESIS CHARACTERISTIC SETTING METHOD

Honorable Commissioner of Patents
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents, cited in the European Search Report and listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner.

In compliance with the concise explanation requirement under 37 CFR §1.98(a)(3), for foreign language documents, Applicant encloses herewith a copy of the European Search Report citing such documents, together with an English-language version (if not already included) of that portion of the Search Report indicating the degree of relevance found by the foreign office.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

I hereby certify that each item of information contained in this Information Disclosure Statement was the first citation of that item by a foreign patent office in a counterpart foreign application, which occurred not more than three months prior to the filing of this statement.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

Respectfully submitted,



Sean M. McGinn

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Date: 2/5/04

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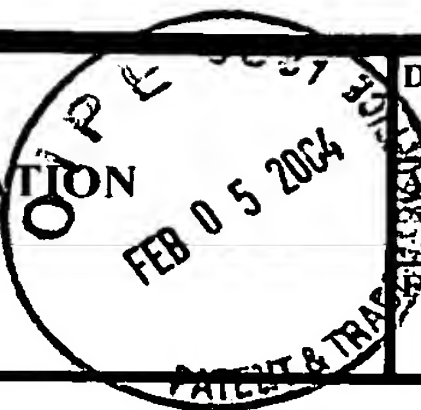
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INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



Docket Number (Optional)

K06-159164M/AT

Application Number

10/668,254

Applicant(s)

Yasuji Norito

Filing Date

September 24, 2003

Group Art Unit

2816

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		6,198,239 B1	03/06/2001	Blackburn			
		6,304,088 B1	10/16/2001	Yee			
		3,642,329	02/15/1972	Zechnull et al.			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
		10-197572	07/31/1998	Japan			(ABS only)	
		FR 2 817 970	06/14/2002	France				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		European Search Report dated January 14, 2004.
		"Voltage Comparator Circuit", IBM Technical Disclosure Bulletin", Vol. 17, No. 4, September 1974, pp. 1151-1152, XP-002265879.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.